

# CIRCULAR

## Ref. No. :AJU/AD/ENGG/043/23-24

#### Date: 09.09.2023

It is for the information of all the Students of 3<sup>rd</sup> Semester B.tech Electrical & Electronics Engineering that the Internal Theory & Practical Examinations (Phase-2) which is schedule from 16<sup>th</sup> Sept-23 as per the Time Table given below..

Prof. Ashwini Kumar Asst Dean School of Engineering & IT ARKA JAIN University, Jharkhand

### Copy for information & necessary action please:-

- 1. PS to The Vice-Chancellor
- 2. PS to The Director
- 3. PS to The Registrar
- 4. Controller of Examination for information
- 5. In charge Web services for Website
- 6. Notice Board
- 7. Guard File

Sr. No	Date	Timing	Subject Name
1	16.09.2023	04:30PM - 05:30PM	Electromagnetic Fields
2	17.09.2023	04:30PM - 05:30PM	Electrical Circuit Analysis
3	19.09.2023	04:30PM - 05:30PM	Analog Electronic
4	20.09.2023	04:30PM - 05:30PM	Electrical Machines-I
5	23.09.2023	04:30PM - 05:30PM	Engineering Mathematics – iii
6	24.09.2023	04:30PM - 05:30PM	Environmental Sciences
7	30.09.2023	12:00PM - 04:00PM	Electrical Circuit Analysis Lab
8	30.09.2023	12:00PM - 04:00PM	Electrical Machines- I Lab
9	30.09.2023	12:00PM - 04:00PM	Analog Electronics Lab

## 3rd Semester internal Examination of B.tech in Electrical & Electronics Engg